

Define an ADC for a low power Battery Fuel Gauge: comparing existing solutions, identify and simulate an optimal topology

Join us at the ADI Milan Design Center:
you will work
in an experienced team
with a personal mentor
interacting with international sites
Scholarship provided!

- ▶ Work with the ADI Product Definer to find the optimal specifications: sampling time, accuracy, current consumption, silicon area, robustness
- ▶ Select one of the possible ADC topologies after literature analysis and review of existing ADI IPs
- ▶ Define a suitable ADC topology, design the schematics and run analog or mixed mode (AMS) simulations using state of the art tools and methodologies
- ▶ You have a good understanding of analog design and can effectively communicate in English. Knowledge of low power ADC a plus
- ▶ You are curious, flexible and proactive



POLITECNICO
MILANO 1863

DIPARTIMENTO DI ELETTRONICA
INFORMAZIONE E BIOINGEGNERIA

CONTACT US !

marco.sampietro@polimi.it
athos.canclini@analog.com